

Docket No.: 49657-904



RECEIVED PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE SEP 28 2001

TECHNOLOGY CENTER 2800

In re Application of

Yoshinori TANAKA, et al.

Serial No.: 09/756,864

Group Art Unit: 2826

Filed: January 10, 2001

Examiner: T. L. Dickey

For: SEMICONDUCTOR DEVICE COMPRISING A HIGHLY-RELIABLE,  
CONSTANT CAPACITANCE CAPACITOR (AS AMENDED)

#7  
10/4/01

AMENDMENT

Commissioner for Patents  
Washington, DC 20231

Sir:

Responsive to the non-Final Office Action dated June 29, 2001, please amend the  
above-referenced application as follows.

IN THE CLAIMS:

Please amend claims 12 and 15 as follows. A clean copy of the claims is  
appended hereto.

12. (Amended) A semiconductor device including a memory cell region and a  
peripheral circuit region, comprising:

a semiconductor substrate having a major surface;

an insulating film, having an upper surface, being formed on said major surface of  
said semiconductor substrate to extend from said memory cell region to said peripheral  
circuit region;